





DDR_D[0..15]	<<DDR_D[0..15]	[1]	Q_IO0	<<Q_IO0	[1]
DDR_A[0..15]	<<DDR_A[0..15]	[1]	Q_IO1	<<Q_IO1	[1]
DDR_BA[0..2]	<<DDR_BA[0..2]	[1]	Q_IO2	<<Q_IO2	[1]
			Q_IO3	<<Q_IO3	[1]
DDR_DM0	<<DDR_DM0	[1]	NRST	<<NRST	[1,3]
DDR_DM1	<<DDR_DM1	[1]	Q_NCS	<<Q_NCS	[1]
DDR_DQS0_N	<<DDR_DQS0_N	[1]	Q_CLK	<<Q_CLK	[1]
DDR_DQS0_P	<<DDR_DQS0_P	[1]			
DDR_DQS1_N	<<DDR_DQS1_N	[1]	Q_EN	<<Q_EN	[3]
DDR_DQS1_P	<<DDR_DQS1_P	[1]			
DDR_CLK_P	<<DDR_CLK_P	[1]			
DDR_CLK_N	<<DDR_CLK_N	[1]			
DDR_NCS	<<DDR_NCS	[1]			
DDR_NWE	<<DDR_NWE	[1]			
DDR_NCAS	<<DDR_NCAS	[1]			
DDR_NRAS	<<DDR_NRAS	[1]			
DDR_NRESET	<<DDR_NRESET	[1]			
DDR_CKE	<<DDR_CKE	[1]			
DDR_ODT	<<DDR_ODT	[1]			

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